



Attorney's Docket No.: 10559-611001/P12850

Handwritten notes: "2666" and a large checkmark.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al.      Art Unit: 2666  
Serial No.: 10/066,297      Examiner: Shick C. Hom  
Filed : February 1, 2002  
Assignee : Intel Corporation  
Title : PROCESSING DATA PACKETS

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

01/23/2006 RHEBRAHT 00000044 10066297

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January 17, 2006  
Date of Deposit

Signature

Handwritten signature of Jennifer H. Payne.

Jennifer H. Payne  
Typed or Printed Name of Person Signing Certificate

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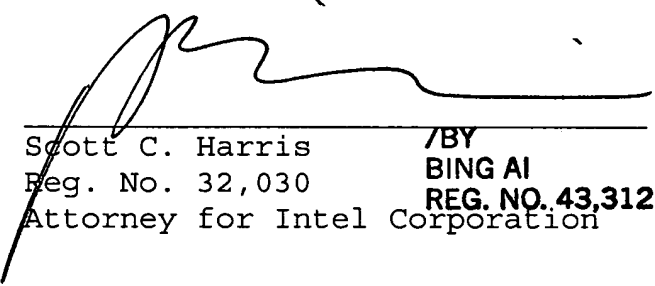
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Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 17, 2006

  
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-611001	Application No. 10/066,297
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
		Filing Date February 1, 2002	Group Art Unit 2666

### U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2001/0014100	08/2001	Abe et al.			
	AB	2002/0131443	09/2002	Robinett et al.			
	AC	2002/0144006	10/2002	Cranston et al.			
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	AAA	6,687,247	02/2004	Wilford et al.			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified) <b>JAN 20 2006</b> INFORMATION DISCLOSURE STATEMENT by Applicant (Use several sheets if necessary) (37 CFR 1.102(b))	U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-611001	Application No. 10/066,297
	Applicant Gilbert Wolrich et al.			
	Filing Date February 1, 2002		Group Art Unit 2666	

## U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,724,721	04/2004	Cheriton			
	ACC	6,731,596	05/2004	Chiang et al.			
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## Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ATT	0 418 447	03/1991	Europe				
	AUU	0 760 501	03/1997	Europe				
	AVV	WO 98/25210	06/1998	WIPO				
	AWW	WO 03/017541	02/2003	WIPO				

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<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 1, 2002	Group Art Unit 2666

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AXX	Adiletta, et al., "The next generation of Intel IXP Network Processors", Intel Technology Journal, Network Processors, vol. 6, issue 3, published Aug. 15, 2002, pp. 6-18.
	AYY	Brewer, et al., "Remote Queues: Exposing Message Queues for Optimization and Atomicity", SPAA '95 Santa Barbara, CA, pp. 1-13.
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	AAAA	Dandamudi, S., "Multiprocessors", IEEE Computer, Mar. 1997, pp. 82-89.
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	ACCC	Jonkers, H., "Queueing Models of Shared-Memory Parallel Applications", Computer and Telecommunications Systems Performance Engineering, Pentech Press, London, 1994, 13 pages.
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	AEEE	Kumar, S., et al., "A Scalable, Cache-Based Queue Management Subsystem for Network Processors", no date, pp. 1-7.
	AFFF	Lymar, T., et al., "Data Streams Organization in Query Executor for Parallel DBMS", no date, 4 pages.
	AGGG	McLuckie, L., et al., "Using the RapidIO Messaging Unit on PowerQUICC III", Freescale Semiconductor, Inc., 2004 Rev. 1, pp. 1-19.
	AHHH	Michael, M., "Scalable Lock-Free Dynamic Memory Allocation", PLDI '04, ACM, Jun. 2004, pp. 1-12.
	AIII	Pan, H., et al., "Heads and Tails: A Variable-Length Instruction Format Supporting Parallel Fetch and Decode", CASES 01, No. 16-17, 2001, 8 pages.
	AJJJ	Scott, M., "Non-Blocking Timeout in Scalable Queue-Based Spin Locks", PODC '02, ACM, Jul. 2002, pp.31-40.

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